

IN THE CLAIMS

1. (currently amended) An ESD protection structure having a single crystal Si-sided diode used to protect an internal circuit, the ESD protection structure electrically connected between an input pad and a node and the internal circuit electrically connected to the node, the ESD protection structure comprising:

an input resistor including a plurality of single crystal Si resistors formed over an insulating material layer, electrically coupled between the input pad and the node, wherein the single crystal Si resistors are arranged in parallel connection; and

at least a single crystal silicon-sided junction diode formed over the insulating material layer, wherein the single crystal silicon-sided junction diode is electrically coupled between one terminal of a corresponding power supply and a node and dominates discharge of ESD current.

2. (original) The structure according to claim 1, wherein the insulating material layer is made of oxide.

3. (original) The structure according to claim 1, wherein the insulating material layer includes a SOI.

4. (original) The structure according to claim 1, further comprising an input buffer electrically coupled between the node and the internal circuit.

5. (original) The structure according to claim 1, wherein the single crystal resistor is made of a single silicon layer on the insulating material layer.

6. (original) The structure according to claim 1, wherein the single crystal Si-sided junction diode includes a P/N junction formed on the insulating material layer.

7. (original) The structure according to claim 1, wherein the single crystal Si-sided junction diode includes a MOS transistor formed over the insulating material layer, and one of the source/drain region of the MOS electrically connects to a gate by a wiring line.

8. (currently amended) The structure according to claim 1, wherein the single crystal Si-sided junction diode ~~diodes~~ comprises:

a first diode, electrically connected between the node and one terminal of a first power supply; and

a second diode, electrically connected between the node and one terminal of a second power supply.

9. (currently amended) An ESD protection structure having a single crystal Si-sided diode used to protect an internal circuit formed from an insulating material layer on a SOI, the ESD protection structure electrically connected between an input pad and a node and the internal circuit electrically connected to the node, the ESD protection structure comprising:

an input resistor including a plurality of single resistors formed over the insulating material layer, wherein each of the single resistors is electrically coupled between the input pad and the node, and the single resistors are arranged in parallel connection; and

at least a single crystal sided junction diode formed over the insulating material layer, wherein the single crystal sided junction diode is electrically coupled between one terminal of a corresponding power supply and a node and dominates discharge of ESD current.

10. (original) The structure according to claim 9, further comprising an input buffer electrically coupled between the node and the internal circuit.

11. (previously presented) The structure according to claim 9, wherein each of the single crystal resistors is made from a single silicon layer on the insulating material layer.

12. (original) The structure according to claim 9, wherein the single crystal Si-sided junction diode includes a P/N junction formed on the insulating material layer.

13. (original) The structure according to claim 9, wherein the single crystal Si-sided junction diode includes a MOS transistor formed over the insulating material layer, and one of the source/drain region of the MOS electrically connects to a gate by a wiring line.

14. (currently amended) A semiconductor structure of ESD protection, the ESD protection electrically connects between an input pad and an integrated circuit, the semiconductor structure comprising:

a semiconductor substrate;

an insulating layer, formed on the semiconductor substrate;

an input resistor including a plurality of single crystal Si resistors, formed over the insulating layer, wherein the single crystal Si resistors are arranged in parallel connection;

at least a single crystal Si-sided junction diode formed over the insulating layer, electrically coupled between one terminal of a corresponding power supply and the integrated circuit and dominating discharge of ESD current;

a first conductive layer, formed over the insulating layer, used to electrically connect one terminal of the input resistor and the input pad;

a second conductive layer, formed over the insulating layer, used to electrically connect another terminal of the input resistor and the integrated circuit; and

a third conductive layer, formed over the insulating layer, used to connect the single crystal Si-sided junction diode and the integrated circuit.

15. (previously presented) The structure according to claim 14, wherein each single crystal Si resistor includes a single crystal silicon layer.

16. (original) The structure according to claim 14, wherein the single crystal sided junction diode includes a single crystal silicon P/N junction.

Claims 17-18: (canceled)

19. (previously presented) The structure according to claim 14, wherein the single crystal Si resistors are isolated by an isolation structure.

20. (original) The structure according to claim 19, wherein the isolation structure includes a shallow trench isolation.

21. (currently amended) An ESD protection structure used to protect an internal circuit, the ESD protection structure electrically connected between an input pad and a node and the internal circuit electrically connected to the node, the ESD protection structure comprising:

an input resistor including a plurality of single crystal Si resistors formed on an insulating material layer, electrically coupled between the input pad and the node, wherein the single crystal Si resistors are arranged in parallel connection; and

a single crystal layer formed over the insulating material layer, wherein the single crystal layer comprises at least two doped regions with different dopant types to form a side junction diode, and the side junction diode is electrically coupled between one terminal of a corresponding power supply and a node and dominates discharge of ESD current.